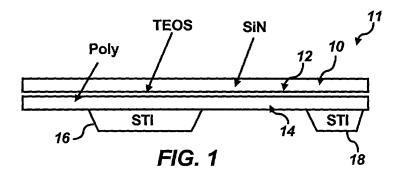
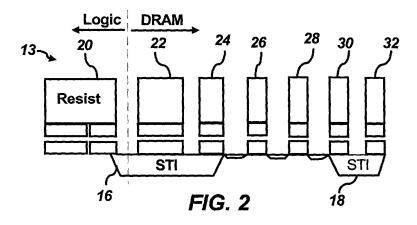
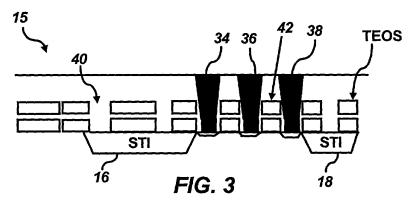
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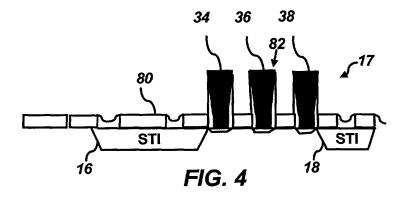


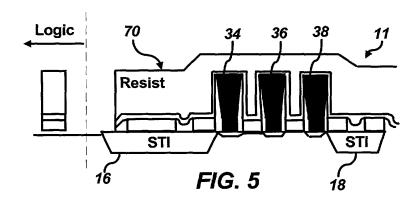


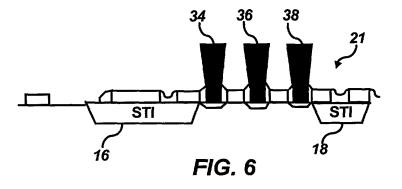


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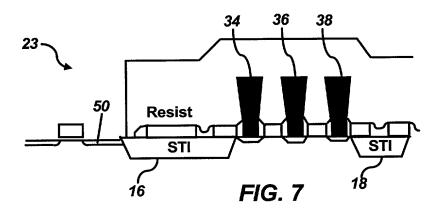


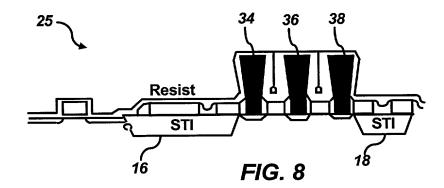
Inventor(s): Min-Hsiung Chiang Serial No.: To Be Assigned

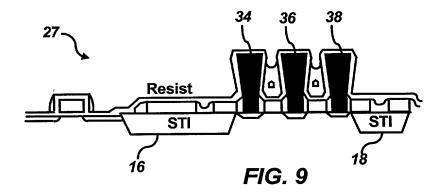
Filed: Herewith

For: Compatible Embedded DRAM Process For MIM Capacitor

Attorney Docket No.: 67,200-547



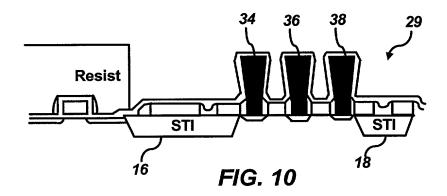


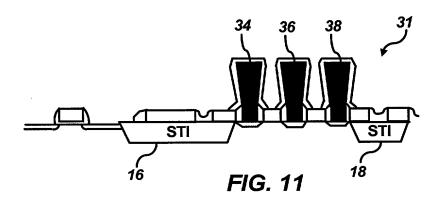


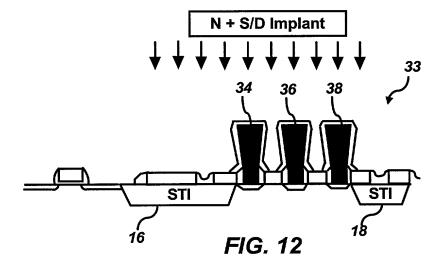
Inventor(s): Min-Hsiung Chiang
Serial No.: To Be Assigned
Filed: Herewith
For: Compatible Embedded DRAM Process For MIM Capacitor

Attorney Docket No.: 67,200-547





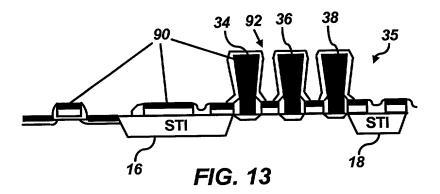


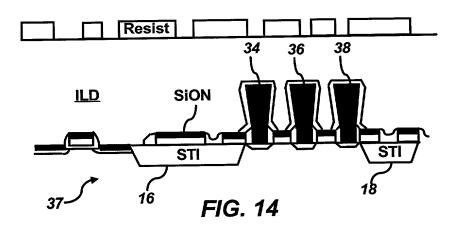


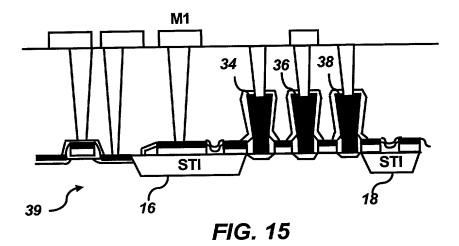
For: Compatible Embedded DRAM Process For MIM Capacitor

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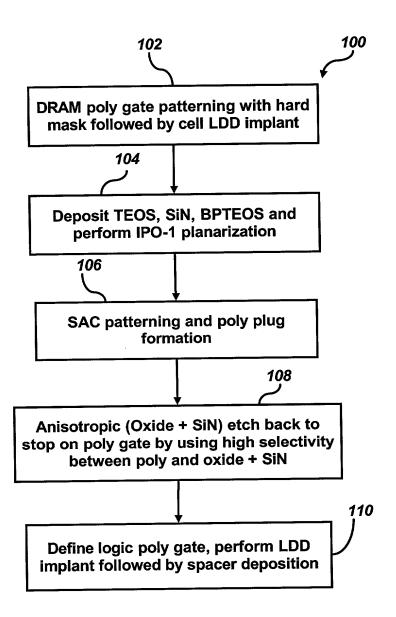


FIG. 16

For: Compatible Embedded DRAM Process For MIM Capacitor

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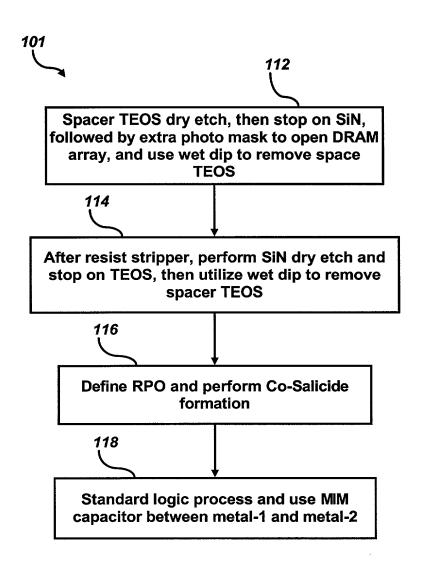


FIG. 17